Amendments to the Specification:

Please amend the paragraph beginning at page 5, line 8 as follows:

The present invention is able to perform power management operations (power consumption control function) for reducing the power consumption after exceeding the maximum output power of a power supply apparatus such as an AC adapter when a system configuration of the computer apparatus such as the notebook PC is maximized by connecting to a PC adapter, a USB apparatus and the like, and operating a plurality of programs are simultaneously.

Please amend the paragraph beginning at page 9, line 10 as follows:

In the computer system 10 shown in Figure 1, a CPU 11 for operating at a predetermined clock frequency for data processing functions as brains for the entire computer system 10, and performs various programs under control of the OS. The CPU 11 is interconnected with various components via three stages of buses, which are an FSB (Front Side Bus) 12 that is a system bus, a PCI (Peripheral Component Interconnect) bus 20 that is a bus for high-speed I/O unit and an ISA (Industry Standard Architecture) bus 40 that is a bus for low-speed I/O unit. The CPU 11 is intended to perform faster processing by storing a program code and data in a cache memory. In recent years, while SRAM of 128 Kbytes or so is integrated as a primary cache in the CPU 11, a secondary cache 14 of 512 Kbytes to 2 Mbytes is placed via a BSB (Back Slide Side Bus) 13 that is a dedicated bus in order to complement insufficient capacity. Moreover, it is also possible to cut down costs by omitting the BSB 13 and connecting the secondary cache 14 to the FSB 12 to avoid a package of a large number of terminals.

2

Please amend the paragraph beginning at page 11, line 6 as follows:

The DMA controller function is a function of performing data transfer between a peripheral such as an FDD (Floppy Disk Drive) and the main memory 16 without involvement of the CPU 11. The PIC function is a function of having a predetermined program (interrupt handler) executed in response to an interruption request (IRQ) from the peripheral. The PIT function is a function of generating a timer signal at a predetermined cycle. In addition, an interface implemented by the IDE interface function has an IDE hard disk drive (HDD) 31 connected and also a CD-ROM drive 32 ATAPI (AT Attachment Packet Interface) connected. An IDE device of another type such as DVD (Digital Versatile Disc) drive can be connected instead of the CD-ROM drive 32. An external memory such as the HDD 31 or the CD-ROM drive 32 is stored in a storage called a "media bay" or a "device bay" in the main unit of the notebook PC, for instance. There are also cases where such standard external memories are mounted in a manner exclusive and also replaceable with other equipment such as the FDD and battery pack.

3